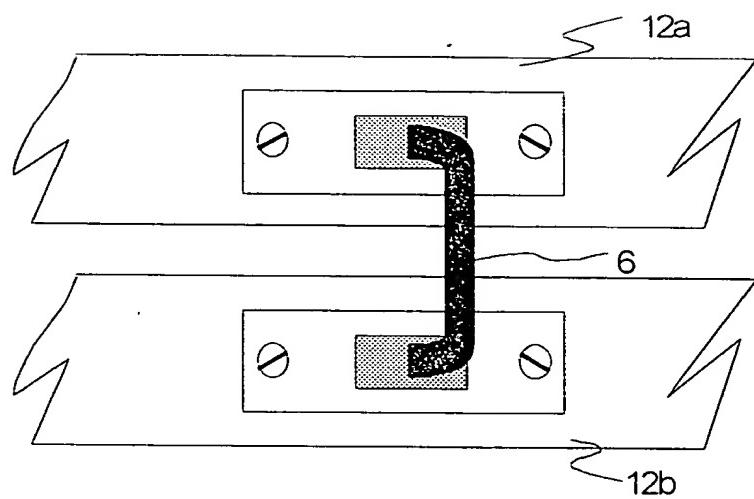
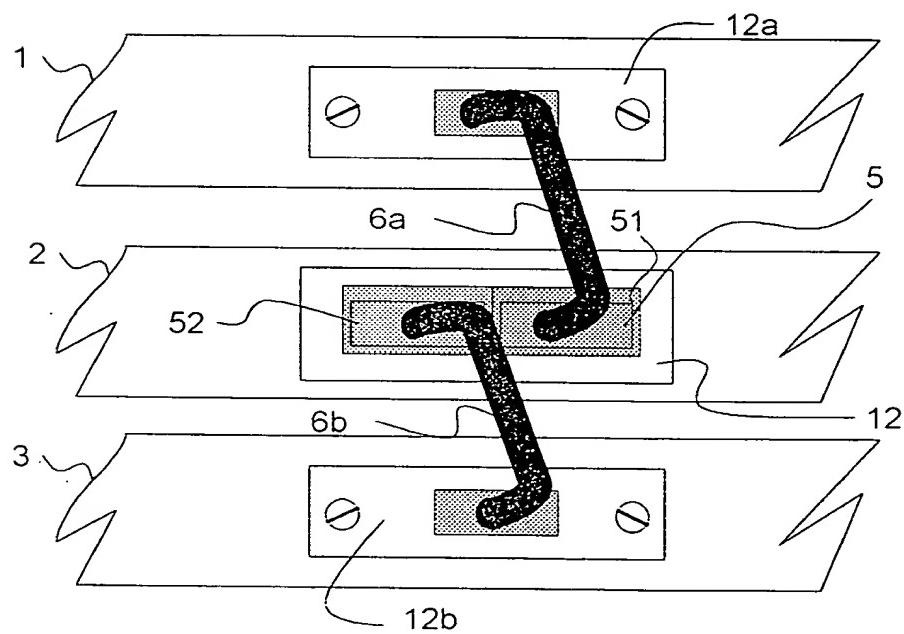


FIG.1

**FIG.2****FIG.3**

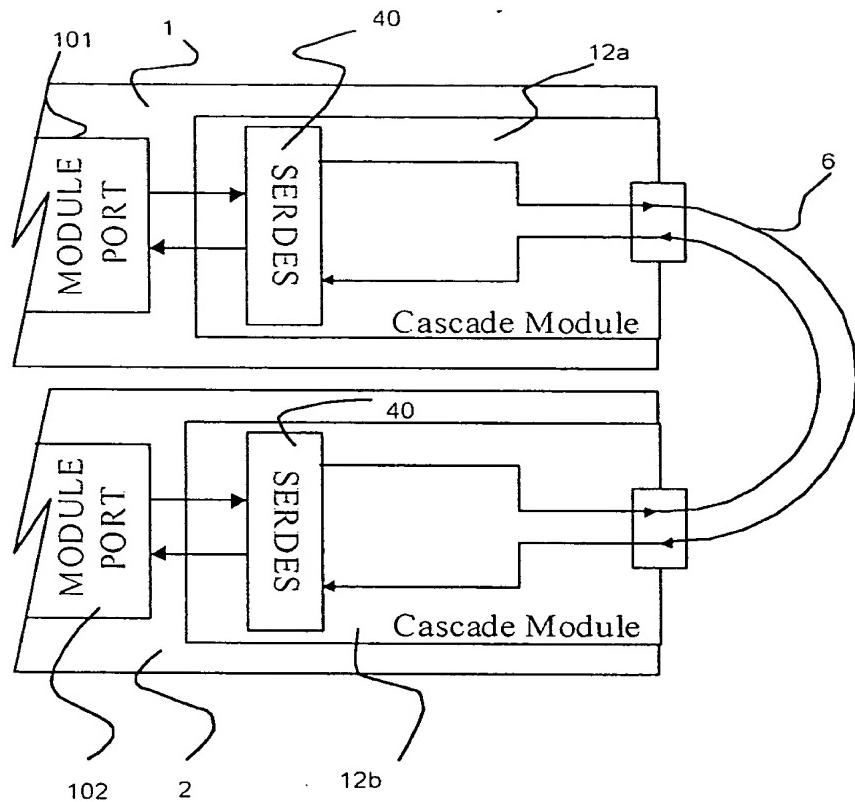
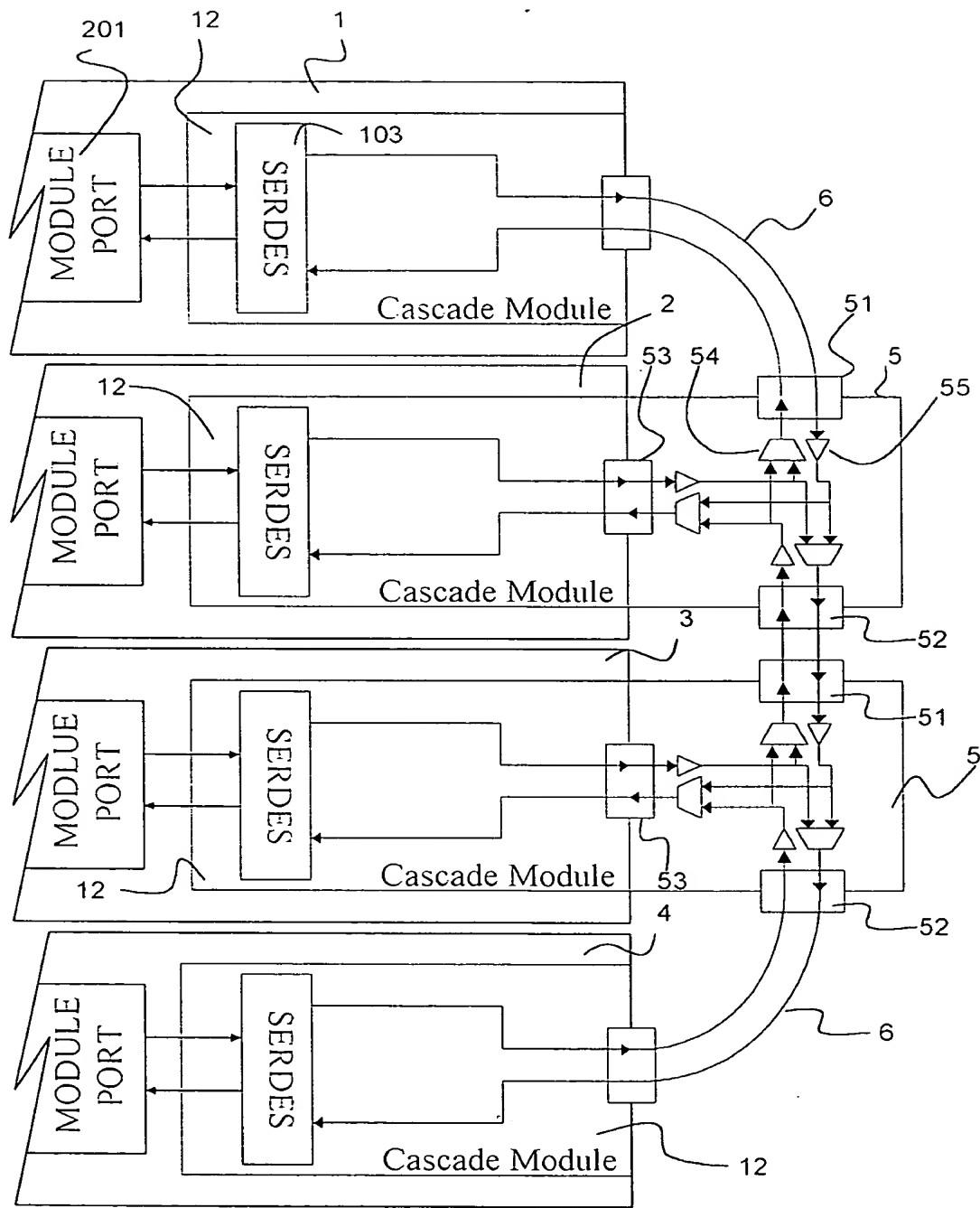


FIG.4

**FIG.5**

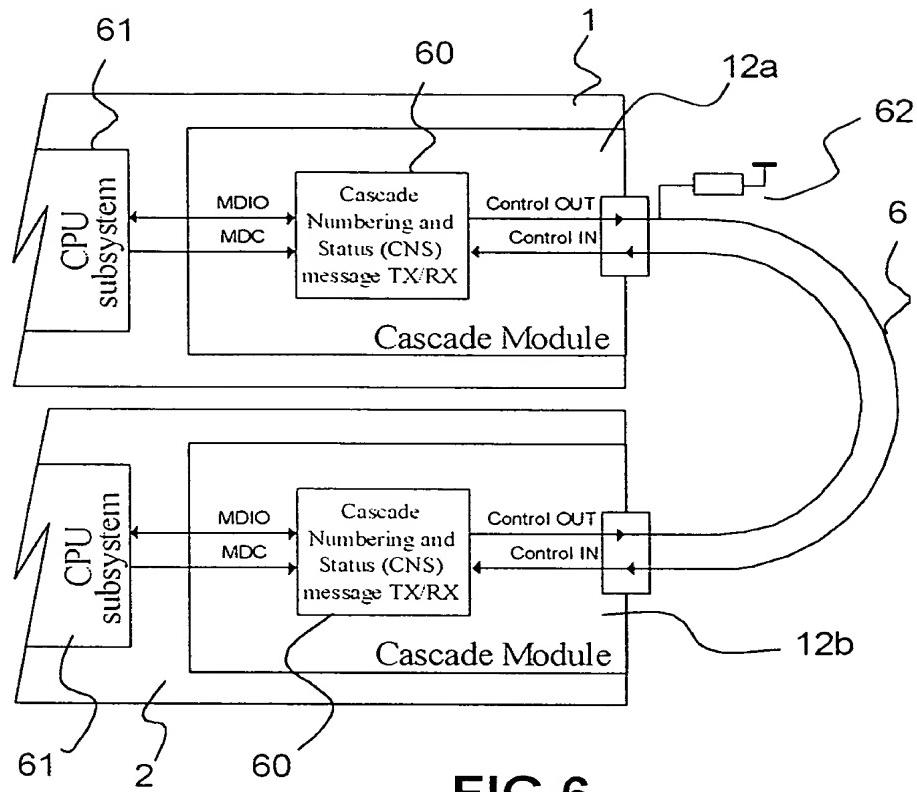


FIG.6

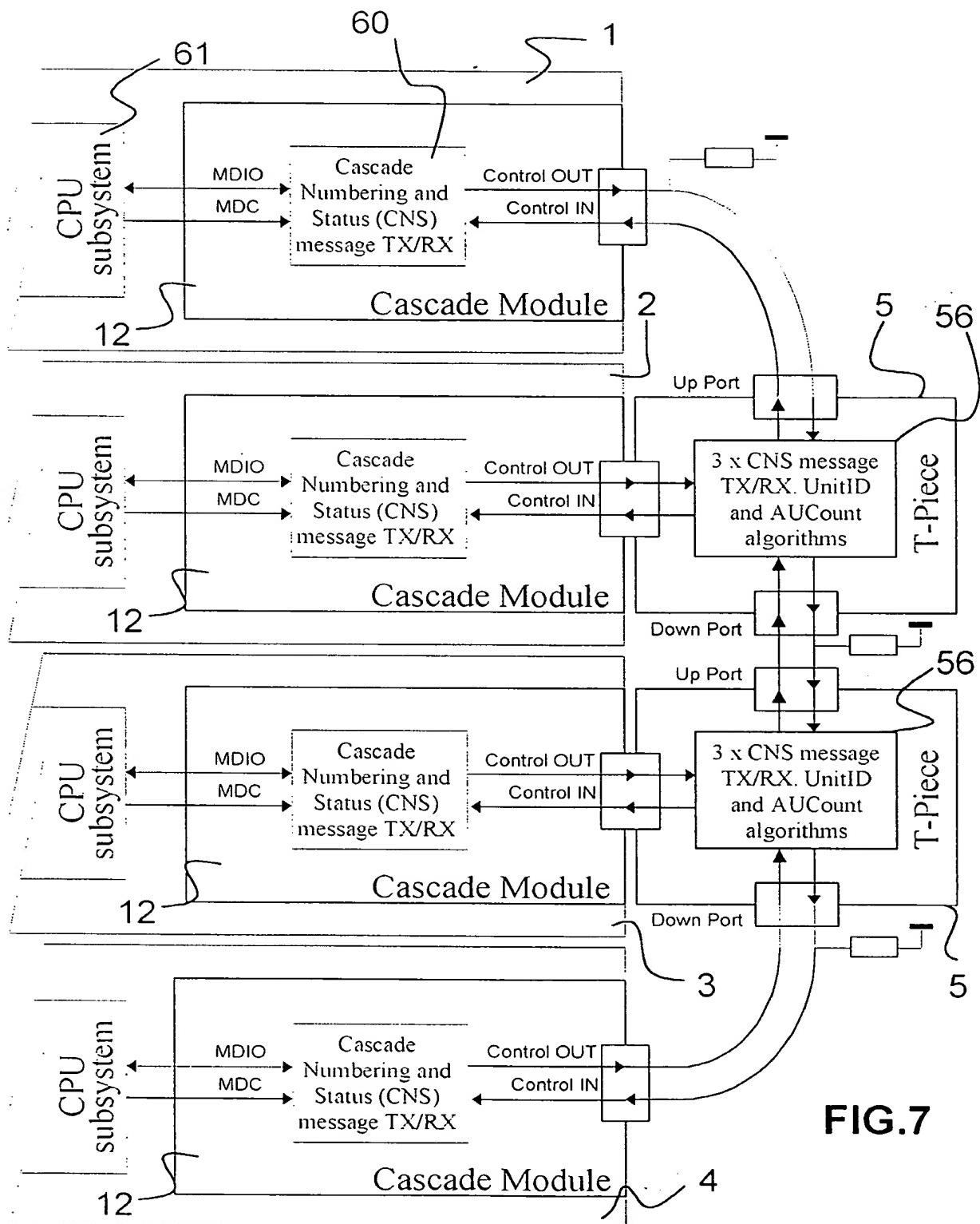
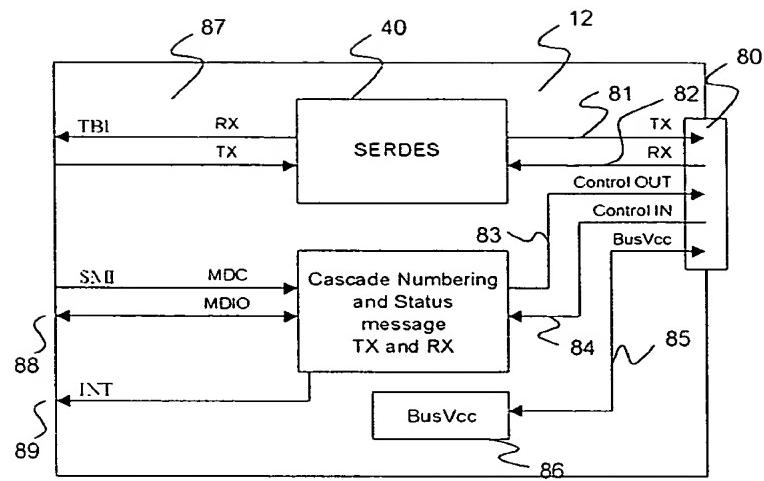
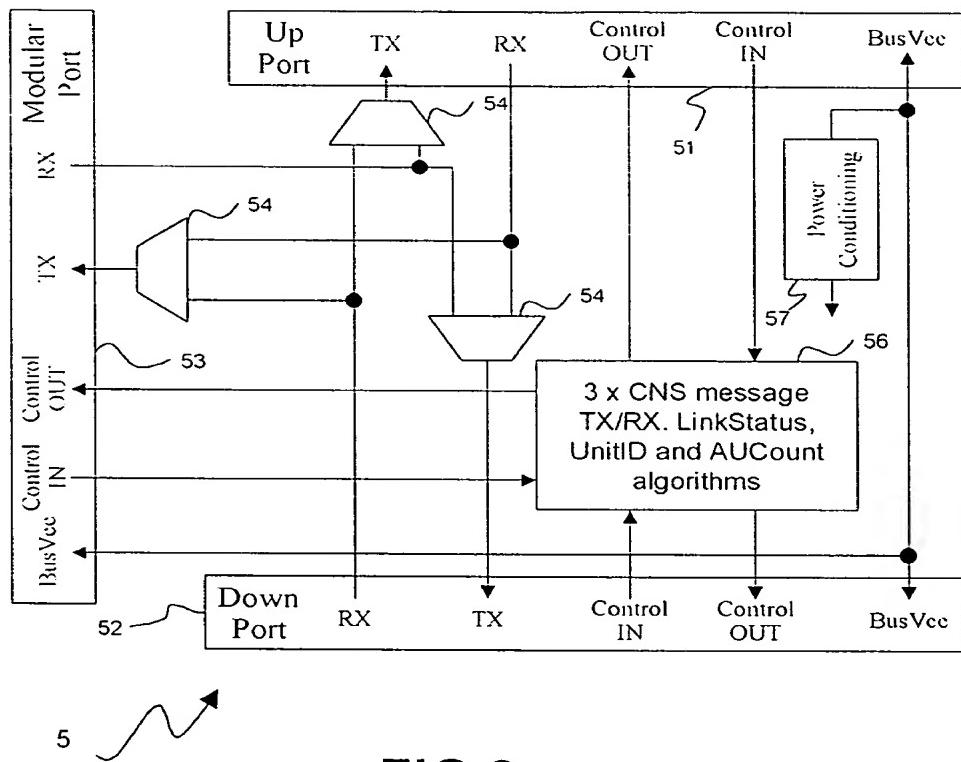
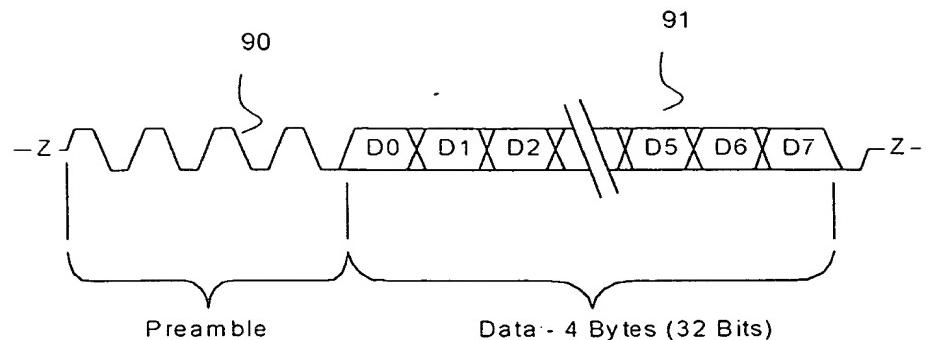


FIG.7

**FIG.8**

**FIG.9**

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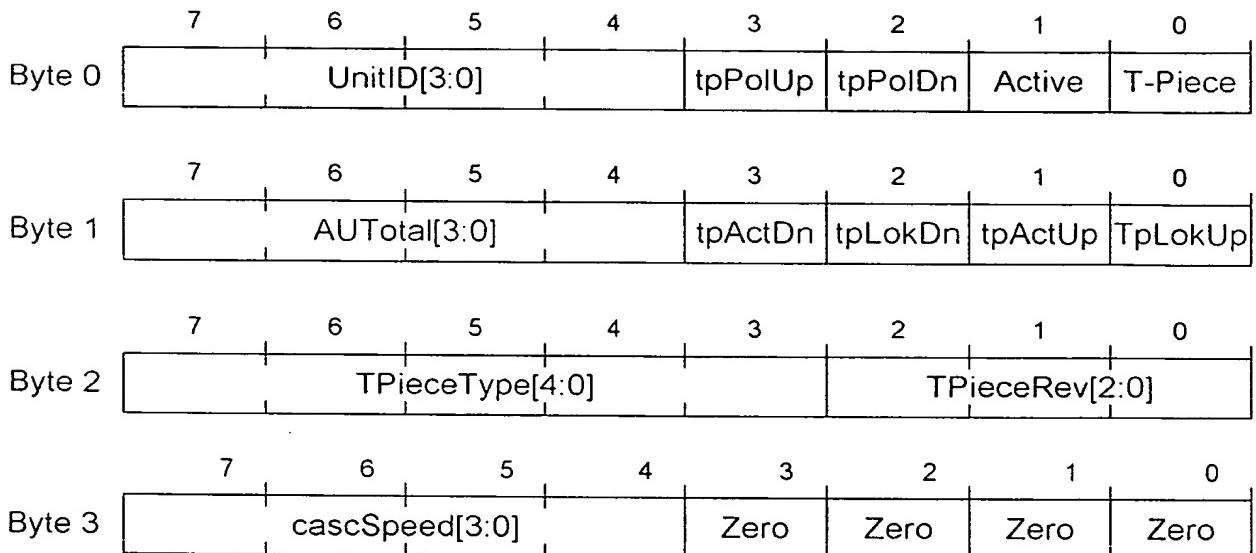


**FIG.10**

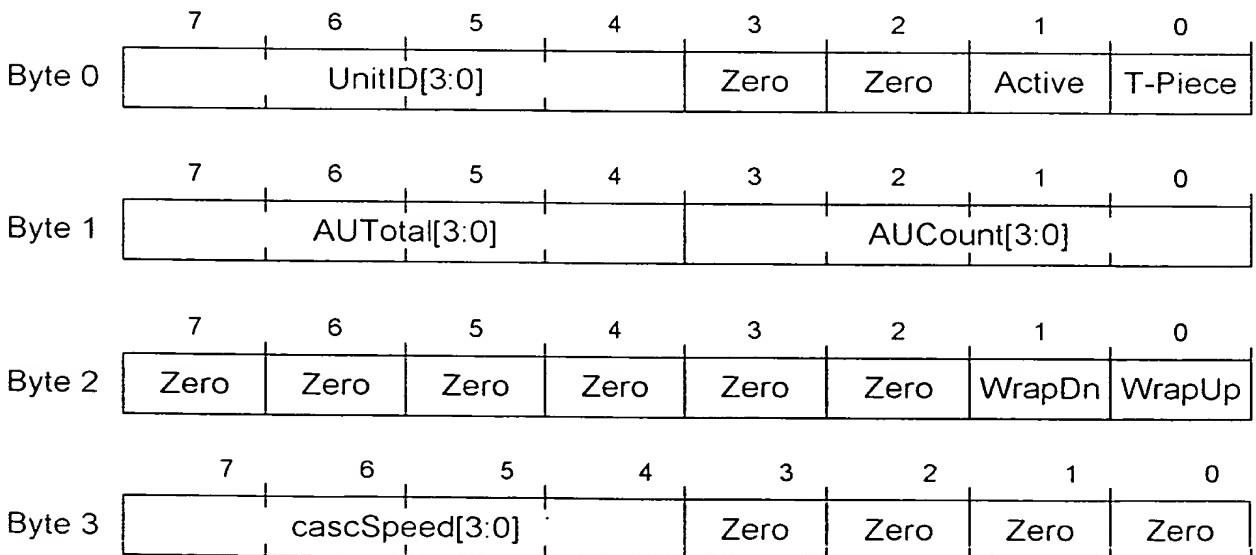
Byte 0	7	6	5	4	3	2	1	0
					Zero	Zero	Active	T-Piece
Byte 1	7	6	5	4	3	2	1	0
					AUTotal[3:0]		AUCount[3:0]	
Byte 2	7	6	5	4	3	2	1	0
	Zero	Zero	Zero	Zero	Zero	Zero	Zero	Zero
Byte 3	7	6	5	4	3	2	1	0
		cascSpeed[3:0]			Zero	Zero	Zero	Zero

**FIG.11**

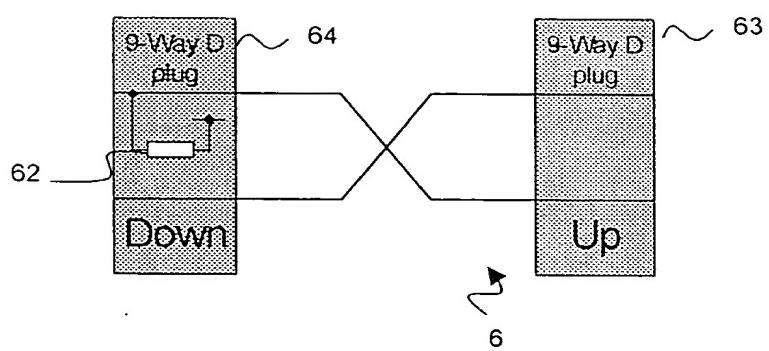
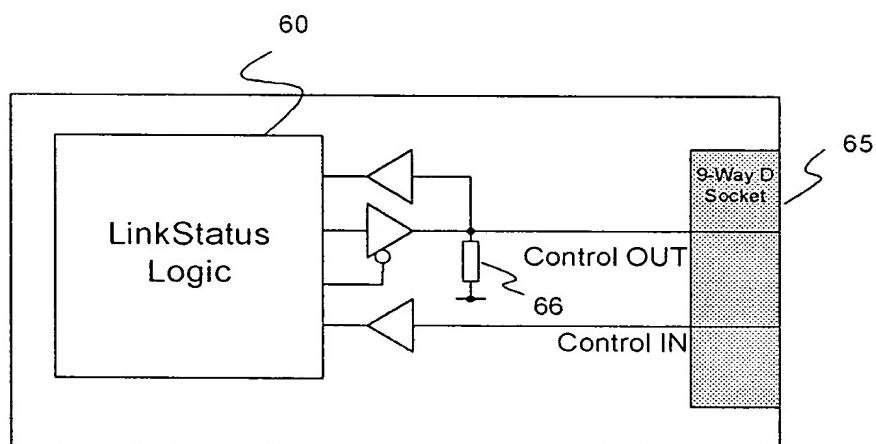
10/19

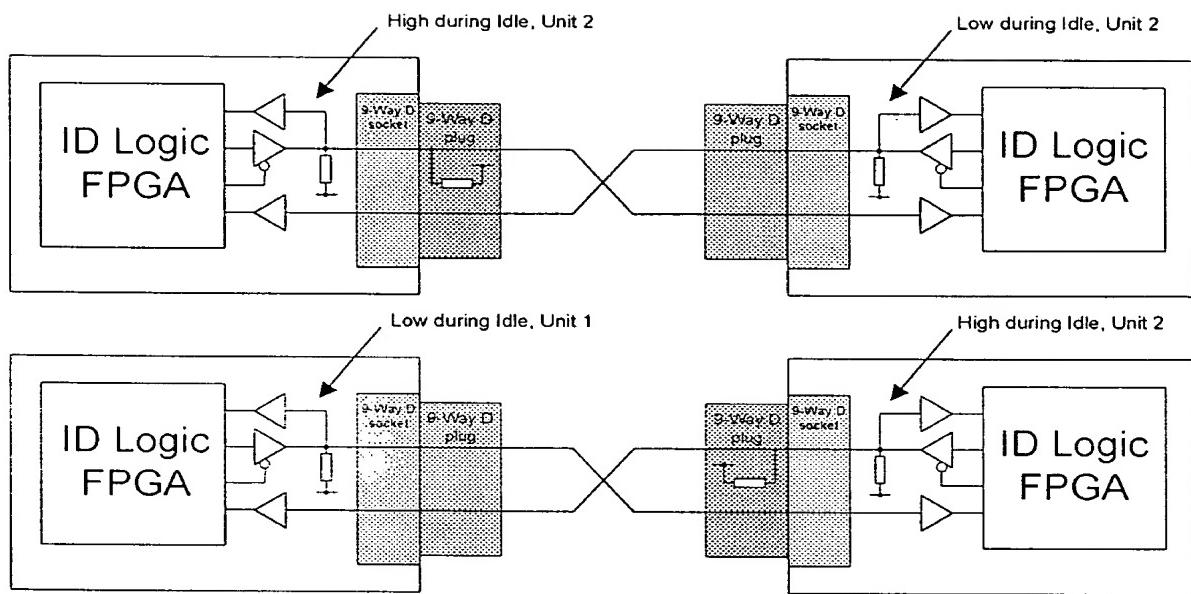


**FIG.12**



**FIG.13**

**FIG.14****FIG.15**

**FIG.16**

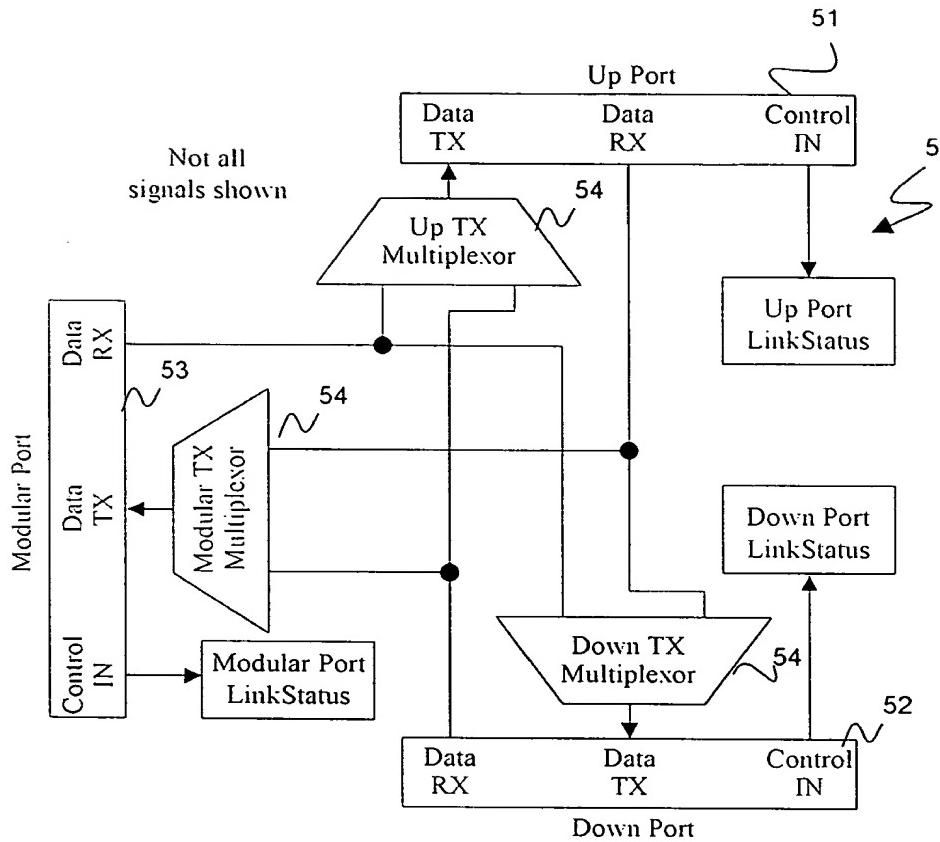
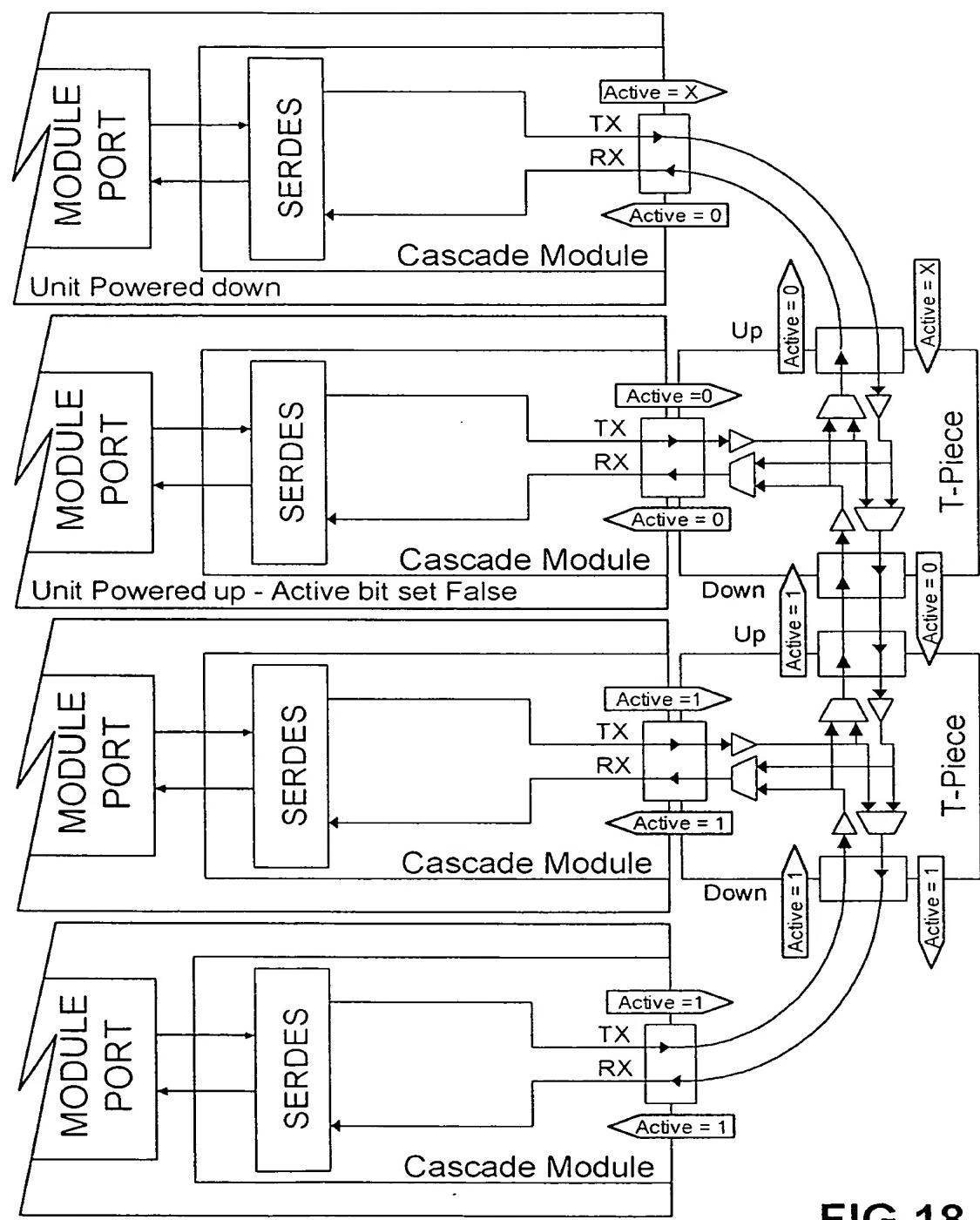


FIG.17

**FIG.18**

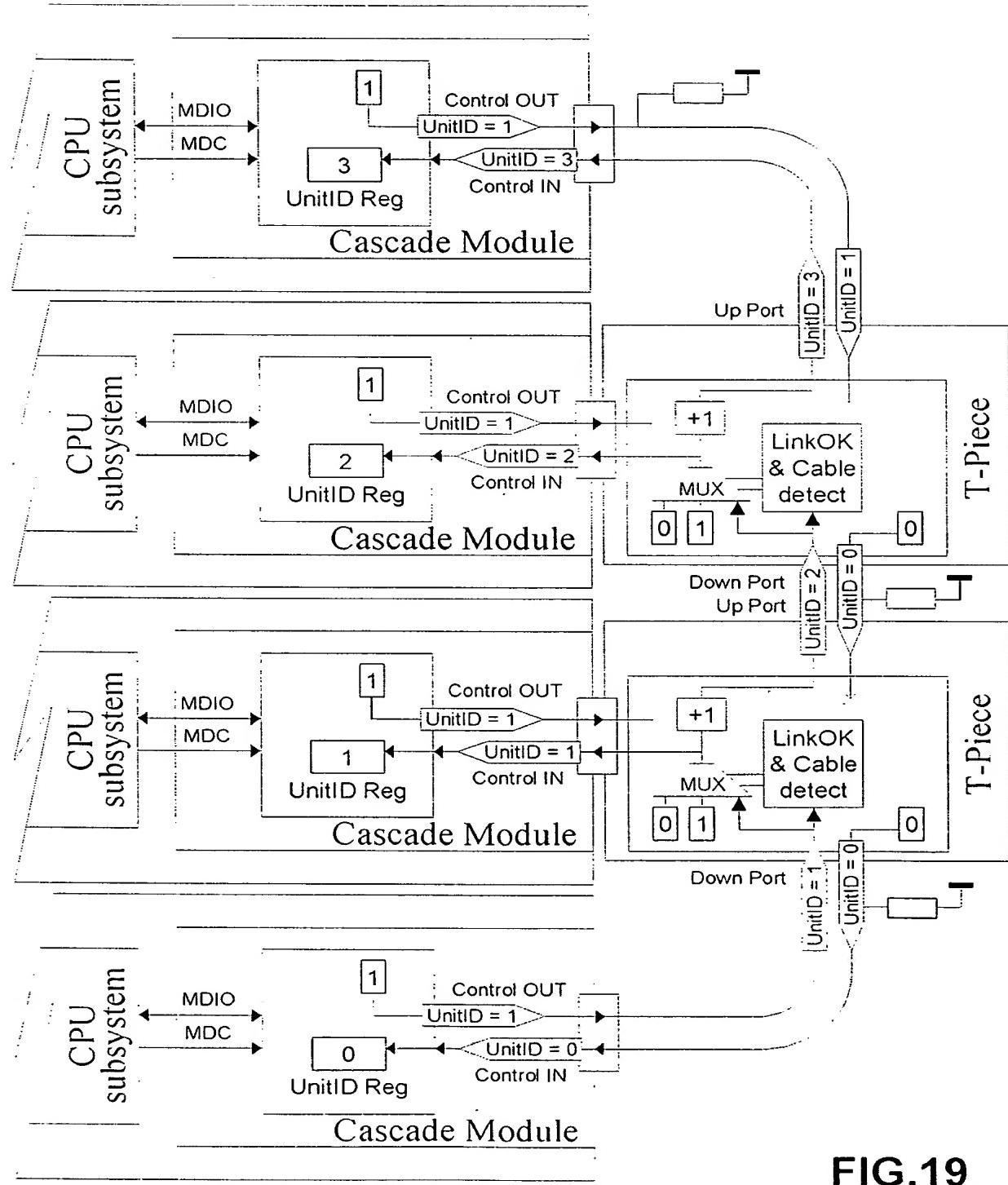


FIG.19

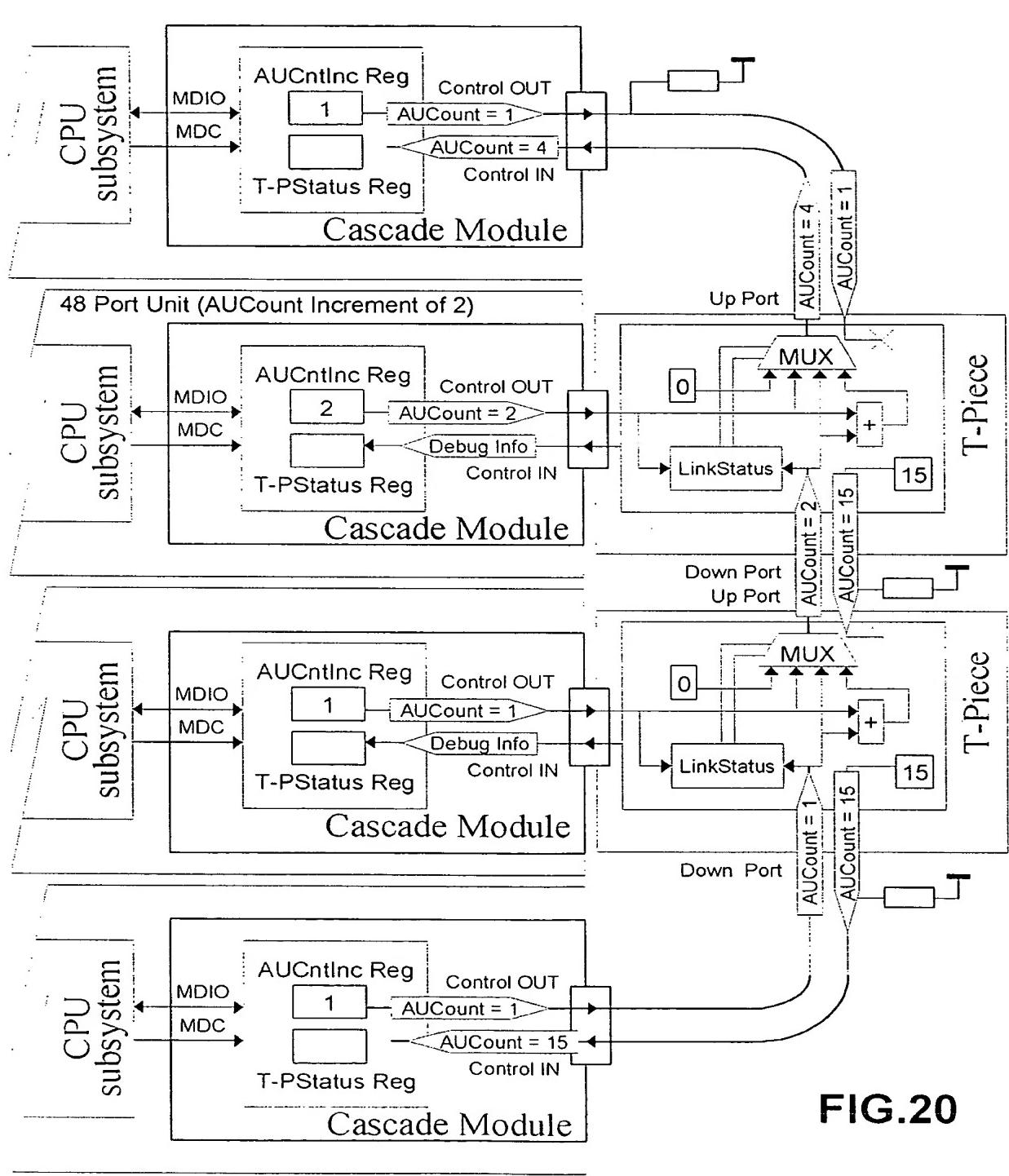
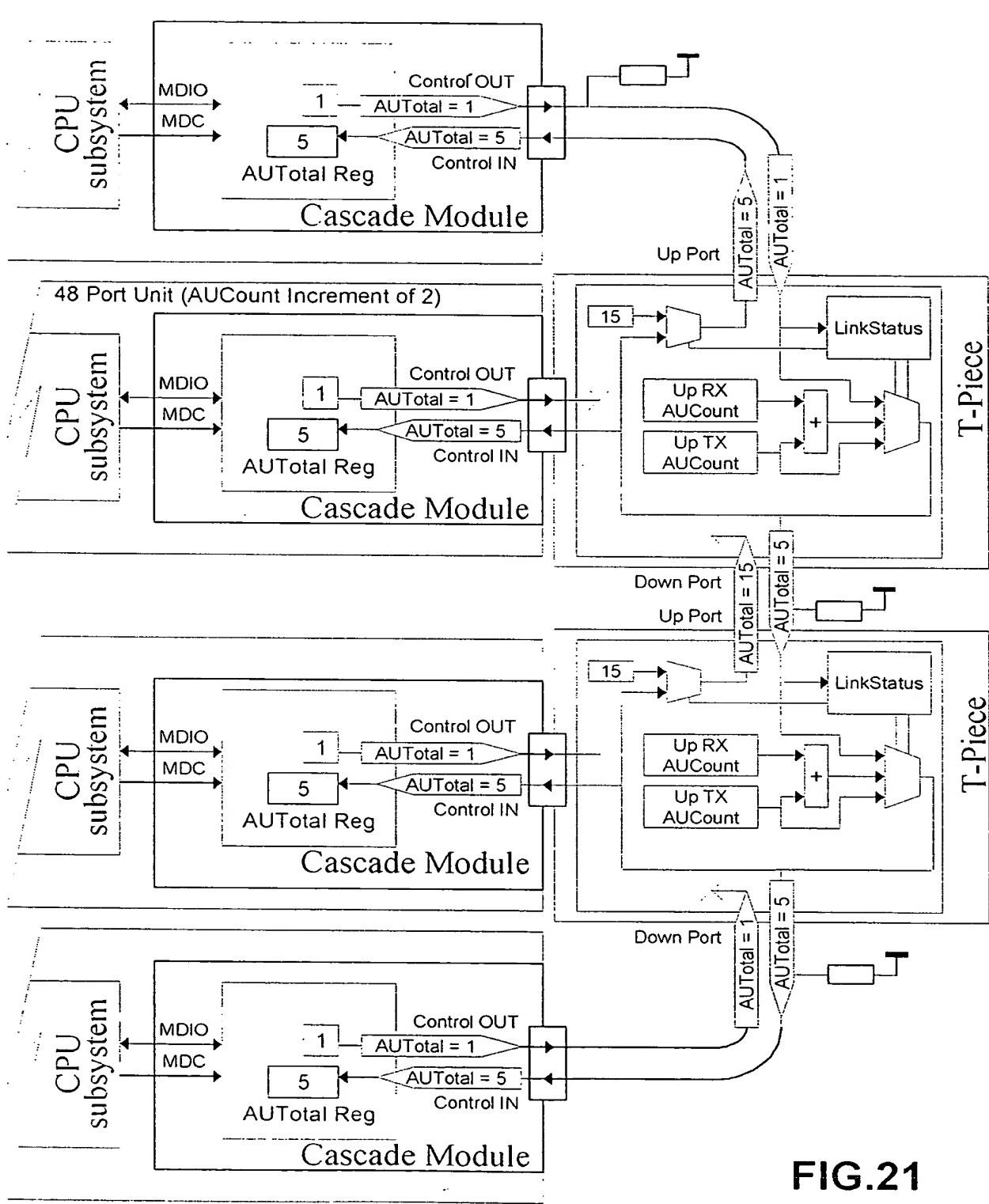


FIG.20



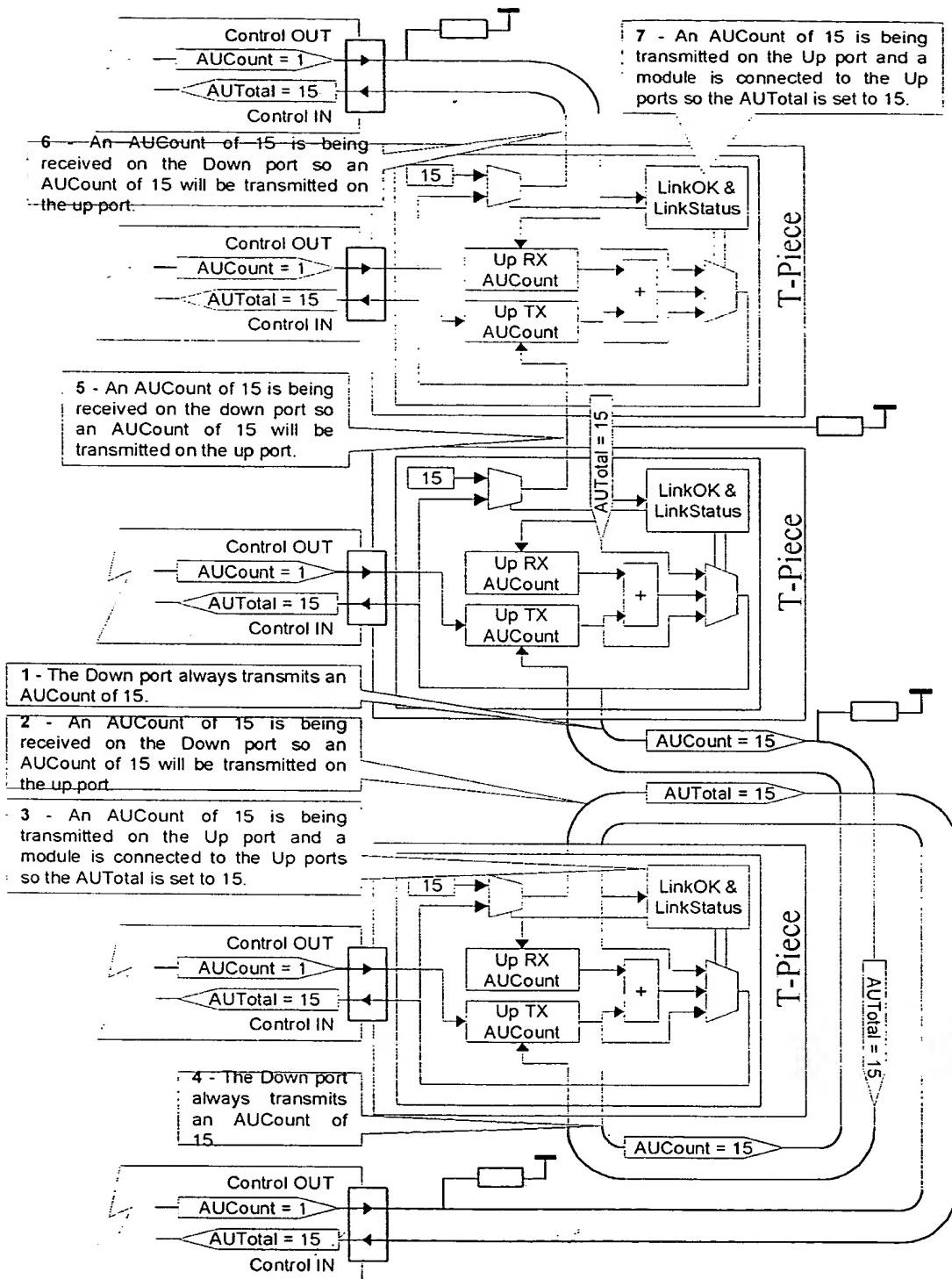
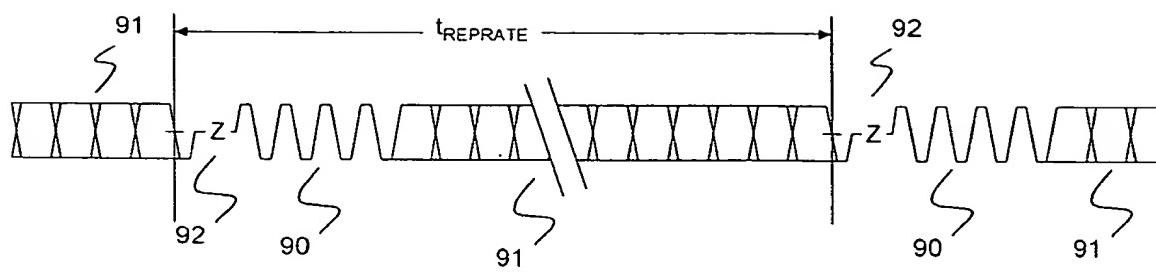


FIG.22

**FIG.23**